

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicant: Kalnitsky Applicant's Ref: 59305-8072.US01	Examiner: Dana Farahani M. Brunsen
Serial No.: 09/882,538) Group Art Unit: 2814 12 31 03
Filed: June 15, 2001)) Date: April 24, 2003
Title: METHOD FOR FORMING A SUPER SELF-ALIGNED HETERO-JUNCTION BIPOLAR TRANSISTOR	•

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on April 24, 2003.

Signed: Valetie A Peterson

AMENDMENT AND RESPONSE TO OFFICE ACTION

Assistant Commissioner for Patents

Washington, D.C. 20231

Dear Sir:

In response to the Office Action dated January 24, 2003, please enter the following amendments and consider the following remarks.

In the Claims

Please amend the claims as follows. Please cancel claims 9, 10-12 and 14 without prejudice.

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